Fig. 1

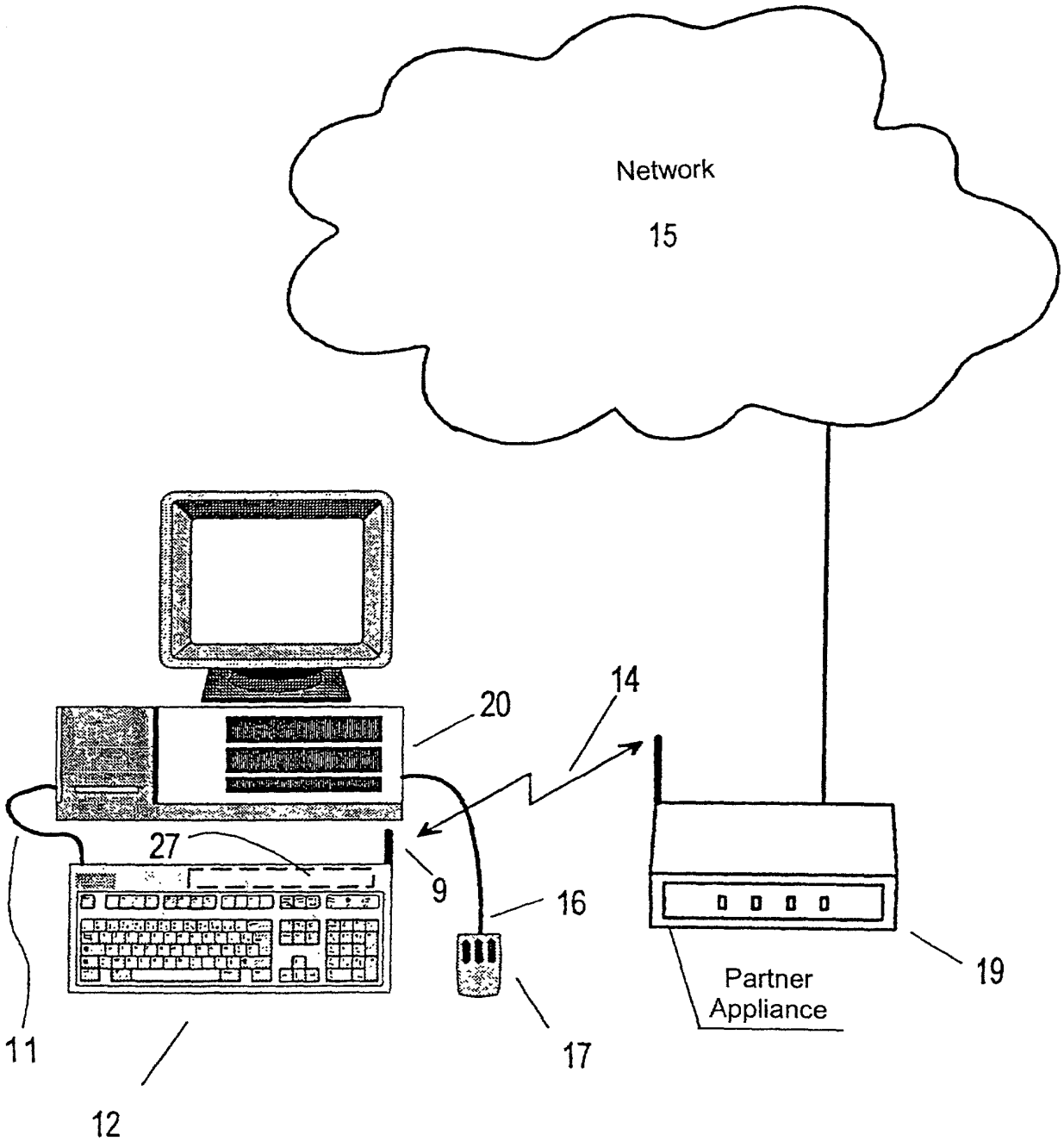


Fig. 2a

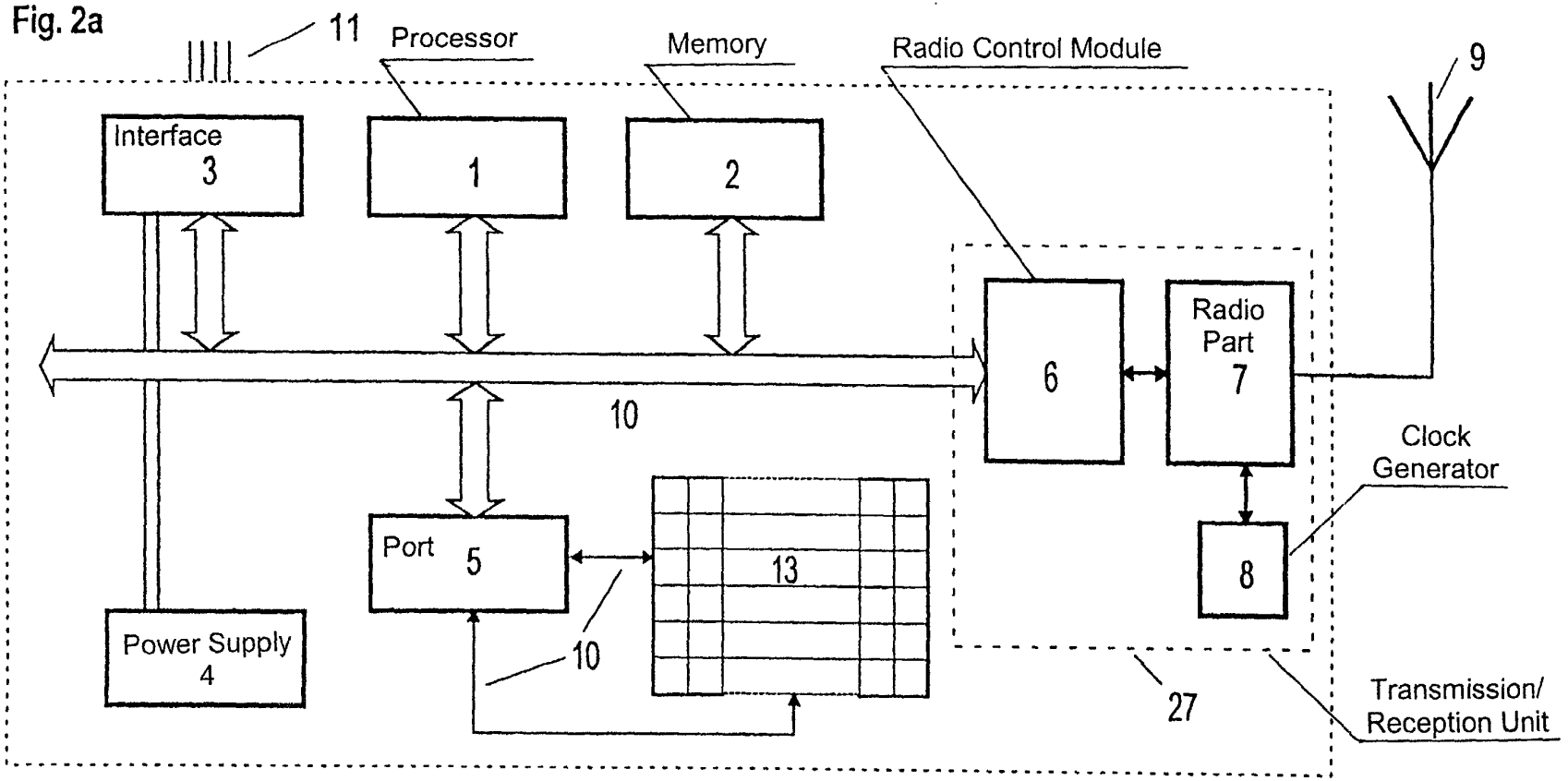

11
Processor
Memory
Radio Control Module
9
Interface
3
1
2
6
Radio
Part
7
10
Clock
Generator
Port
5
13
8
Power Supply
4
10
27
Transmission/
Reception Unit

Fig. 2b

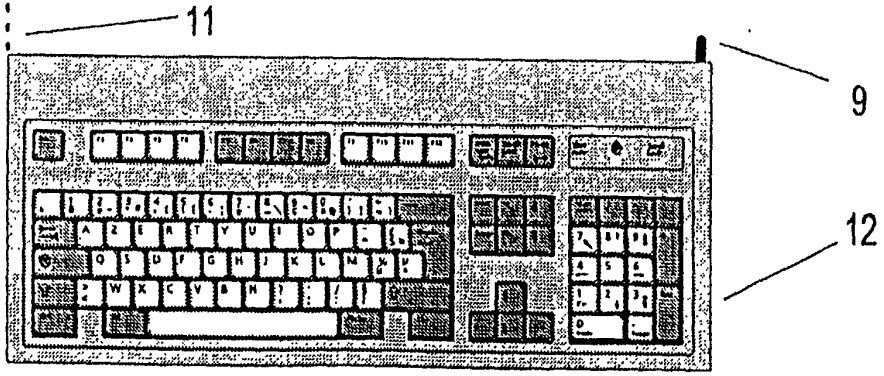
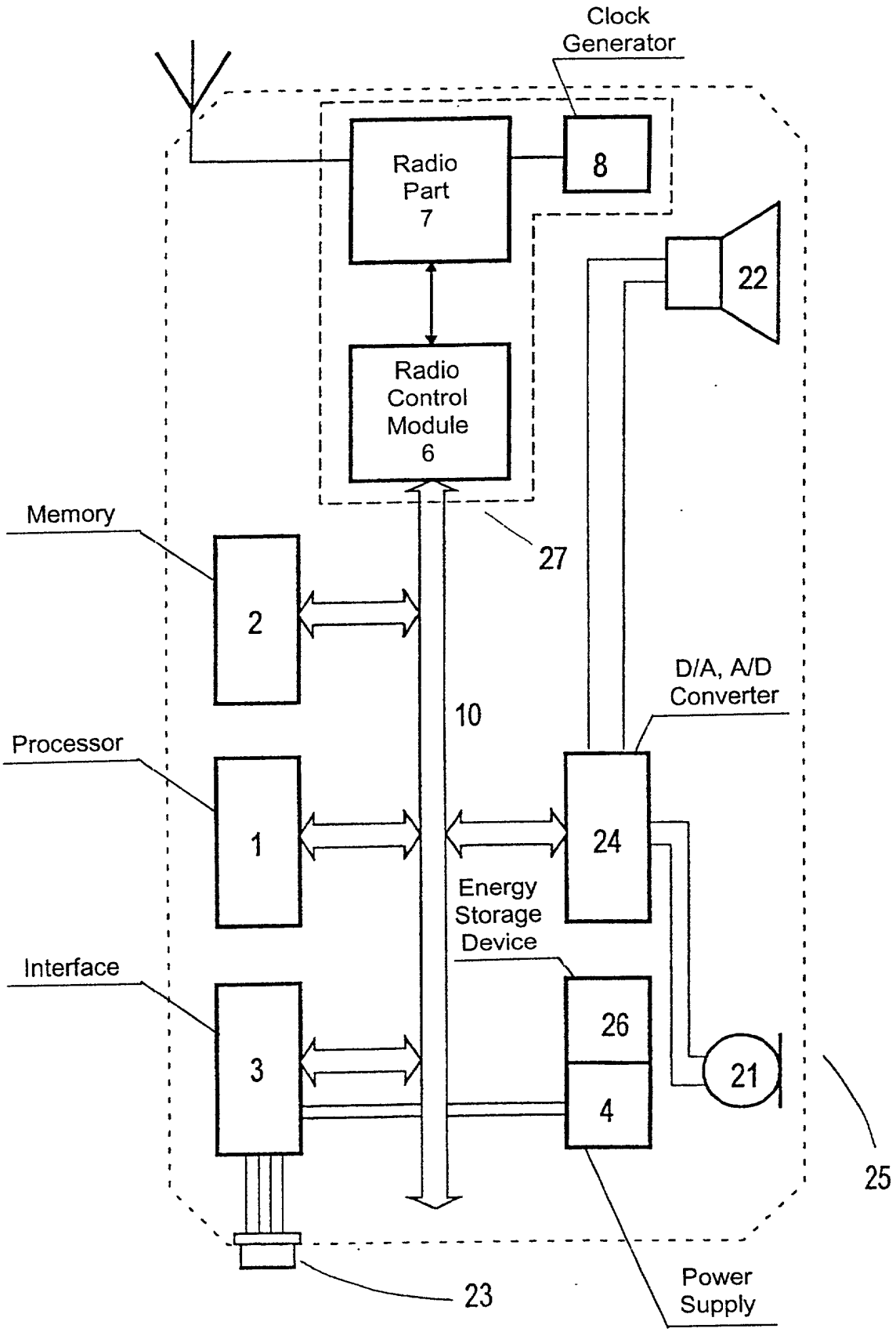

11
9
12

Fig. 3


Clock Generator
Radio Part 7
8
22
Radio Control Module 6
27
Memory
2
10
D/A, A/D Converter
Processor
1
24
Energy Storage Device
Interface
3
26
21
4
25
23
Power Supply